

# FreeRTOS port

## Objective:

Understanding the FreeRTOS Portable Layer.

The objective of this assignment is to understand the FreeRTOS portable layer and how it allows FreeRTOS to run on various hardware architectures. By the end of this assignment, students should be able to explain the role of the portable layer and demonstrate their understanding by analyzing an existing port, specifically the POSIX port.

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## Tasks:

### 1. Research and Study:

- Read the FreeRTOS documentation on the portable layer.
- Study the implementation of the portable layer for an existing architecture, such as ARM Cortex-M (found in `FreeRTOS/Source/portable/GCC/ARM_CM4F`).

### 2. Explain the Portable Layer:

- Write a detailed report (2-3 pages) explaining the role of the FreeRTOS portable layer. Include the following points:
  - What is the portable layer in FreeRTOS?
  - Why is the portable layer necessary?
  - Key components of the portable layer (e.g., context switching, stack management, and ISR handling).
  - How the portable layer interfaces with the core FreeRTOS kernel.
  - **Use diagrams**

### 3. Explain a specific Port:

- Choose an existing port (e.g., ARM Cortex-M4) and analyze its implementation.
  - Choose a CPU architecture you are familiar with; you can even pick POSIX
- Identify and study the following components:
  - Context switch implementation.
  - Stack initialization for tasks.
  - Interrupt handling and how FreeRTOS interacts with the hardware.
  - Any architecture-specific optimizations.

### 4. Submit Your Work:

- Submit the following:

- The detailed report on the FreeRTOS portable layer.
  - The analysis of the existing port.
  - The explanation of the port you selected.
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## Grading Criteria:

- **Report Quality**
    - Clarity and completeness of the explanation.
    - Depth of understanding demonstrated.
    - Accuracy and detail in identifying key components of the existing port.
  - **POSIX Port Explanation**
    - Correctness and completeness of the explanation of the POSIX port.
    - Clarity and depth of the analysis.
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## Additional Resources:

- [FreeRTOS Documentation](#)
  - [FreeRTOS Porting Guide](#)
  - [FreeRTOS Kernel Source Code](#)
  - [FreeRTOS POSIX Port Documentation](#)
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Revision #1

Created 8 months ago by [Preet Kang](#)

Updated 8 months ago by [Preet Kang](#)